

NEURAL NETWORK FOR IMAGE PROCESSING AND ANALYSIS BASED ON FPGA TECHNOLOGIES

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One of the promising areas in the field of image processing and analysis is the hardware implementation of neural network for processing and analyzing of images based on FPGA technologies. The structural scheme of the multifunctional calculator was developed. It contains $M \times N$ cells in the matrix form, a shaping block of features that has N control nodes, input of clock impulses, reset input, output of the common feature of zero and outputs of the zero features by columns and rows of matrix. The described structure is a classifier and it is programmed in the FPGA crystal. Classifier works in two modes: training (adaptation) mode and operational mode.

The main task is to provide processing and analysis of images in real time. Hardware implementation may have a lot of applications: biomedical engineering, aerospace etc. Required time operational characteristics of the system is ensured by parallelism of data and parallel execution of image processing operations. Before image getting to the input of neural network structure is considered image obtained from the camera is preprocessed on a microprocessor. It should accelerate the operation of the device and allow to allocate more memory of FPGA for the classifier.

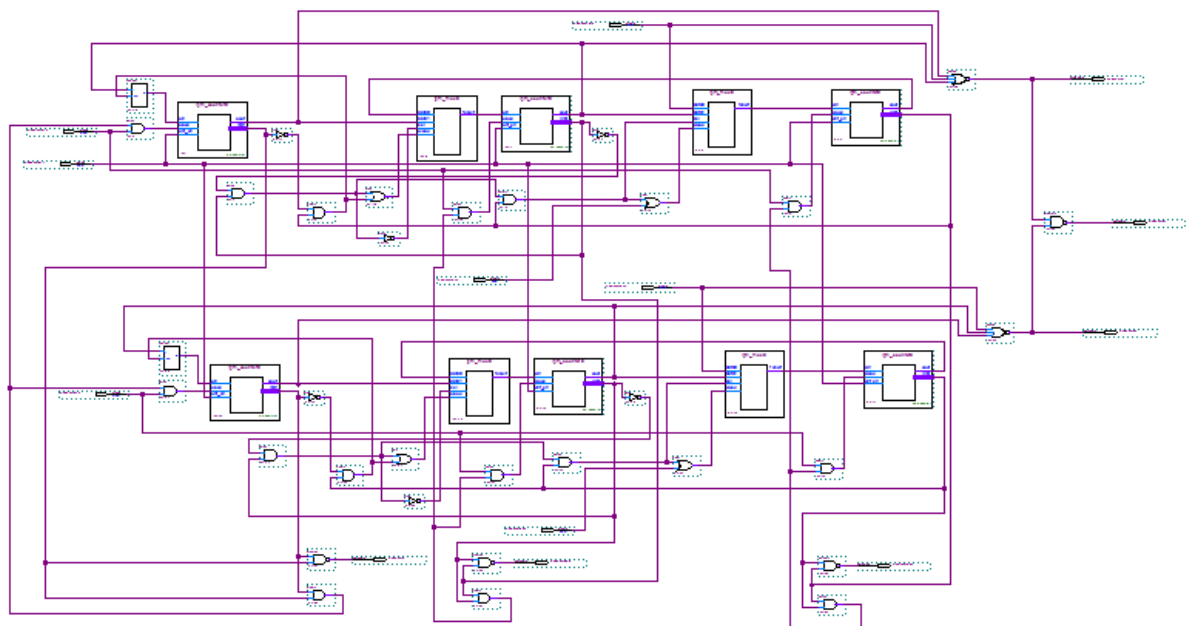


Fig. 1 - Functional scheme of the classifier.

A classifier is programmed in the FPGA crystal whose structure is shown in Fig. 1, which plays a major role, as the result of its functioning is the classification vector.

The classifier works in two modes: learning mode (adaptation) and working mode. In the learning mode (adaptation) through the input block the values of the components of the training vector X_k , multiplied by the weight coefficients w_{ij} , are sequentially fed. At the first step, weights w_{ij} have initial values, and in the process of learning are consistently configurable. In the classification block, the weighted learning signals $w_{ij} x_i$ are processed, which are fed to its inputs in the form of a matrix of size $M \times N$, where M is the number of classes of images, N is the dimension of the input vectors.

In the operating mode inputs are fed to the investigated N -dimensional object (image) Z , the multiplication of each element of the vector Z is performed on the corresponding integer values of the weights corresponding to it, which in the form of a matrix of weights W of size $M \times N$ were formed in the mode of learning and stored. We obtain an input vector in the form of a matrix A_0 of size $M \times N$, which forms an M -dimensional signal of the input object Z to a certain class of M of defined classes. The signal of the end of the classification process is the presence of a single "End" signal.

In future classifier is modified to increase the productivity and processing speed. This can be achieved by increasing the number of logical cells.

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