

# Application of optoelectronic components in intelligent systems

Tatiana Martyniuk<sup>a</sup>, Sergii Pavlov<sup>a</sup>, Andrii Kozhemiako<sup>a</sup>, Leonid Kupershtein<sup>a</sup>, Mykola Tarnovskyi<sup>a</sup>,  
Ivan Met<sup>a</sup>, Vitalina Puhach<sup>b</sup>, Cezary Kaczmarek\*<sup>c</sup>, Bakhyt Yeraliyeva<sup>d</sup>

<sup>a</sup>Vinnitsia National Technical University, 95 Khmel'nyts'ke Hwy, Vinnitsia, Ukraine; <sup>b</sup>Vinnitsia Educational and Scientific Institute of Economics, West Ukrainian National University, 37 Honty St, Vinnitsia, Ukraine; <sup>c</sup>Lublin University of Technology, Nadbystrzycka 38A, 20-613 Lublin, Poland; <sup>d</sup>Dulaty University, 7 Suleymenov St., Taraz, Kazakhstan

## ABSTRACT

The creation of modern intelligent systems based on artificial intelligence requires solving the complex problem of high-speed processing of large amounts of information. The article substantiates the direction of perspective development of optoelectronic means combining electronic processing with optical input/output of data arrays. An example of the implementation of an optical channel classifier based on smart-pixel matrices is given as part of an optoelectronic classifier. Each smart-pixel matrix has electrical control inputs and the ability to process the input optical signals electronically with the output of the result in the form of optical signals.

**Keywords:** optoelectronics, smart-pixel matrix, optical interconnection, intelligent system

## 1. INTRODUCTION

The solution of modern problems related to the parallelization of computing processes in intelligent systems is impossible without the involvement of optical and optoelectronic technologies [1-4]. This is due both to the implementation of alternative approaches to image representation and processing [5-7] and to the creation of optoelectronic processors with three-dimensional interconnects. In such processors, optical connections and optoelectronic elements are used along with electrical ones to organize the exchange of information [8].

## 2. BACKGROUND AND RESEARCH METHODS

As shown by the results of placing processor structures in FPGA chips for complex computing procedures, the only drawback is the significant amount of filling the chip's I/O ports. Therefore, when increasing the dimension of the internal element matrix, a situation is possible when the internal resources of the chip are not fully utilized, but the number of I/O resources is not enough for parallel input of the entire computational matrix.

Thus, the actual solution to the problem of parallel input of large volumes of two-dimensional data arrays into the processor remains the use of optoelectronic means for which the transmission and switching of large amounts of data with a bandwidth of more than 1Tbps is effective [1].

In addition, modern developments in the field of optoelectronic integrated circuits (IC) are characterized by the combination of information input/output in optical form, and internal processing in electrical form. Such optoelectronic IC are called "smart-pixel matrices" and are based on matrixes of vertical-cavity surface-emitting lasers (VCSELs) [9-11].

**Purpose of work.** The goal of this study is to show the possibilities of using optoelectronic elements in modern intelligent systems.

## 3. FEATURES OF THE USE OF OPTOELECTRONIC INTEGRATED CIRCUITS

The use of optoelectronic IC based on smart-pixel matrices largely depends on their main characteristics, namely: the density of smart-pixels in the matrix, the total number of optical inputs/outputs, functional complexity, and overall performance [9-10].

\* c.kaczmarek@pollub.pl; phone +48 81 538 4337

Thus, high-performance (more than 100 Mbps) and with a small number of optical inputs/outputs (10-104) optoelectronic IC are used in switching systems, optoelectronic interconnect boards, and specialized high-performance computing devices. Matrices with a large number of optical inputs/outputs (more than 105) are used in displays, analog optical processors, optoelectronic neural networks, and optical storage devices [9-11].

Moreover, the use of electro-optical light modulators based on liquid crystals, transparent ferroelectric ceramics, and quantum-dimensional structures [8] as part of smart-pixel matrices provides the functions of electrically controlled spatial time light modulators and storage matrices with optical reading of information and the possibility of targeted overwriting of information by electronic means [1,12].

#### 4. APPLIED ASPECTS OF THE USE OF OPTOELECTRONIC MEANS

At present, there are developments of an optical vector-matrix multiplier, which actually implements optical information processing in the optical path of an optoelectronic classifier (Fig. 1) [12].

The optical channel of the classifier (Fig. 1) consists of a matrix of light emitters, a controlled mask, a matrix of photodetectors and microlens matrices. To simplify the consistency of individual components, there is a need for a single manufacturing technology for such important elements of the classifier optical channel as the light emitter matrix and the photodetector matrix. Under this condition, the problem of consistency of the optical channel components will be simplified to matching light and photoelectric matrices with a spatial-time light modulator (STLM) as a controlled mask. The separately proposed version of the optical channel of the optoelectronic classifier is shown in Fig. 2 [12].

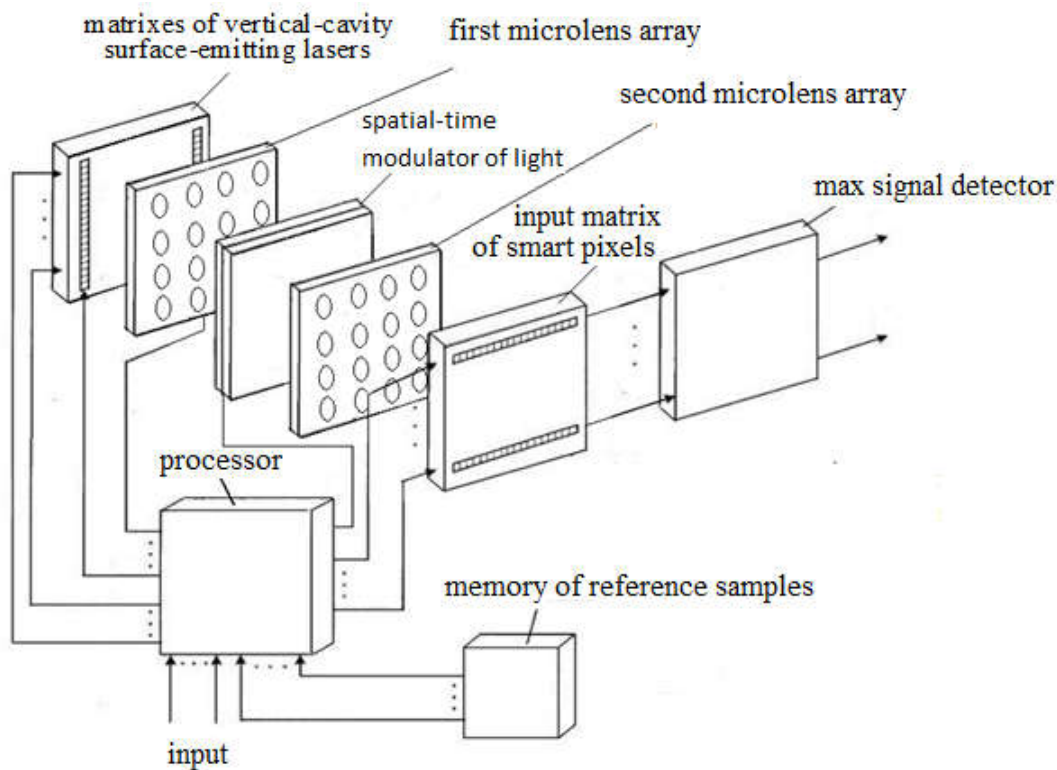


Figure 1. Structure of an optoelectronic classifier.

Thus, the optical channel of an optoelectronic classifier contains an optically coupled light emitting array STLM, a photodetector array, and two microlens arrays located between the light emitting array and the STLM and between the STLM and the photodetector array. The input and output information of the optical channel is transmitted electronically, and the operations that take place inside the channel are transmitted optically.

This makes it expedient to use smart-pixel matrices [10-12] as light and photoelectric matrices of the optical channel of an optoelectronic classifier (Fig. 1).

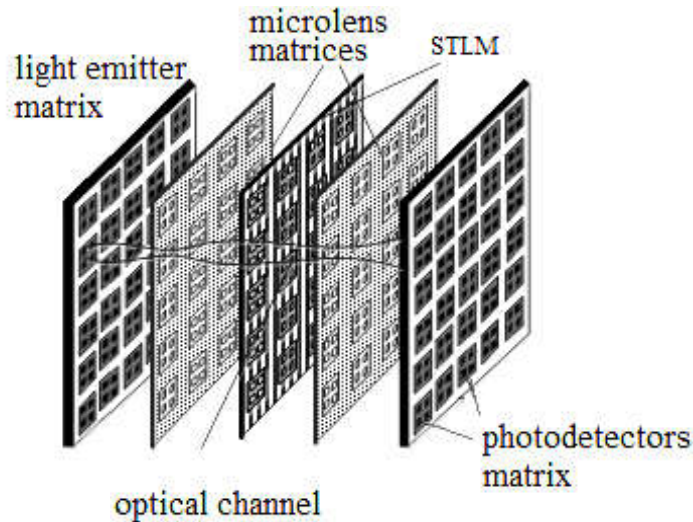


Figure 2. Optical channel of the optoelectronic classifier.

An alternative to optoelectronic means based on smart-pixel matrices can be considered optoelectronic computing environments with a dynamically changing architecture, the basic unit of which is a photorefractive reversible storage medium [2,13,14].

At the same time, developments in the field of creating 3-D optical IC and computing structures [3,12], optoelectronic frequency-dynamic models of neuroelement, and efficient optical information transmission channels between optical IC allow us to approach the construction of promising basic computing facilities for intelligent systems on the basis of the latest optical element base, taking into account known architectural solutions.

## 5. RESULTS

Thus, among the promising areas are, first of all, optical applications in neural networks [15], hybrid intelligent systems with artificial intelligence [16], and optoelectronic processors for image recognition [12]. In addition, the use of optoelectronic means and 3-D optical communication channels will effectively solve the problems that arose during the previous implementation of projects to solve such applied problems as, for example, neural networks and optoelectronic processors for processing multilevel images [17,18,19].

Thus, in combination with the optoelectronic element base, parallel methods of image processing and analysis and pattern recognition allow building highly efficient means for multi-channel processing and switching of dynamic images in real time. Among the advantages of the optoelectronic implementation of computing components is the possibility of parallel processing and switching of two- and multidimensional data arrays of large dimensionality in real time. Among the disadvantages are the complexity of data presentation and high requirements for hardware reliability and accuracy.

## 6. CONCLUSION

Taking into account the growing requirements for parallelism of data processing, the need to create massive interconnections and the use of large amounts of memory, there is no alternative to the promising development of optoelectronic means that combine the advantages of optics with the capabilities of electronics and are associated with the further development of well-known models that are focused on the creation of processor systems with advanced functional and intellectual properties.

## ACKNOWLEDGEMENTS

The research was supported by the grant of the National Research Foundation of Ukraine 2022.01/0135

## REFERENCES

- [1] Intelligent optical backplanes for parallel computing and communications [Online]. Access: [http://www.doe.carleton.ca/~gallan/pdf/optical\\_backplanes.pdf](http://www.doe.carleton.ca/~gallan/pdf/optical_backplanes.pdf).
- [2] Dan, Y., et al., "Optoelectronic integrated circuits for analog optical computing: Development and challenge," *Frontiers in Physics* 10, 1064693 (2022).
- [3] Iga, K., "Vertical-Cavity Surface-Emitting Laser: Its Conception and Evolution". IOP Publishing (2008). <https://doi.org/10.1143/jjap.47.1>
- [4] Klimek, J., "Coupled energy measurements in multi-core photonic crystal fibers," *Metrology and measurement systems*, 20(4), 689-696 (2013). DOI 10.2478/mms-2013-0059
- [5] Kozhemyako, V. P., Martyniuk, T., and Kozhemyako, O., "Vector-matrix conversions for parallel information processing in logic-time base" *SPIE Selected Papers from the International Conference on Optoelectronic Information Technologies*. <https://doi.org/10.1117/12.429706>. (2001)
- [6] Buda, A., Martyniuk, T., Buda, S., "Methods of representation of the symmetric images in devices of recognition" *Proc. SPIE*, 4425, 70–75 (2001).
- [7] Rutkowski, L., [Computational Intelligence. Methods and Techniques], Springer-Verlag, Berlin-Heidelberg (2008).
- [8] Osinsky, V. I., "Information conception of image perceptron at solid-state lighting," *Semiconductor Physics, Quantum Electronics & Optoelectronics*, 10(3), 30-43 (2007).
- [9] Pan, Z. G., Jiang, S., Dagenais, M., Morgan, R. A., Kojima, K., Asom, M. T., Focht, M. W., [Optical injection induced polarization bistability in vertical-cavity surface-emitting lasers], AIP Publishing (1993). <https://doi.org/10.1063/1.110264>. (1993)
- [10] Maleev, N. A. et al., "Matrices of 960-nm vertical-cavity surface-emitting lasers," *Semiconductors* 45, 818-821. (2011). <https://doi.org/10.1134/S1063782611060133>
- [11] Khurgin, J. B., and Sun, G., [Comparative analysis of spasers, vertical-cavity surface-emitting lasers and surface-plasmon-emitting diodes], Springer Science and Business Media LLC (2014). <https://doi.org/10.1038/nphoton.2014.94>
- [12] Fyodorov, V. B., "High-performance optoelectronic switching network with vertical-cavity surface-emitting laser arrays," *Quantum Electronics* 33(3), 259-264 (2003). DOI 10.1070/QE2003v033n03ABEH002397
- [13] Kozhemiako, A. V., Martyniuk, T. B., Kiriyachenko, A. O., and Lyubich, S. P., "Variants of realization of optical processing channels in optoelectronic processors for image recognition," *Optoelectronic Information and Energy Technologies*, 1(27), 183-192 (2014). [In Ukrainian]
- [14] Denz, C., Dellwig, T., Lembcke, J., and Tschudi, T., "Parallel optical image addition and subtraction in a dynamic photorefractive memory by phase-code multiplexing," *Optics letters* 21(4), 278-280 (1996). <https://doi.org/10.1364/OL.21.000278>
- [15] Zakharov, S. M., "The Thermal Cross-Interference Effects in the Arrays of Vertical-Cavity Surface-Emitting Lasers," *Semiconductors*, 35(4) (2001). <https://doi.org/10.1134/1.1365201>
- [16] Ronzitti E., Emiliani V., & Papagiakoumou E. "Methods for Three-Dimensional All-Optical Manipulation of Neural Circuits," *Cellular Neuroscience* 12 *Frontiers Media SA*. (2018). <https://doi.org/10.3389/fncel.2018.00469>
- [17] Medsker, R., [Hybrid intelligent systems], Springer Science & Business Media (2012).
- [18] Krasilenko, V. G., Martyniuk, T. B., Zabolotnaya, N. I., Dubchak, V. N., "Digital optoelectronic processor of multilevel images," *Engineering Simulation* 11(3), 385–394 (1993).
- [19] Wójcik, W. et al. "Information Technology in Medical Diagnostics II," Taylor & Francis Group. CRC Press, Balkema Book, London (2019).